

Appl. No. 10/022, 708

Amtd. Dated November 4, 2005

Reply to Office Action of July 12, 2005

AES 11/8/07

On page 13, 1st full paragraph, of the Specification, please amend:

Marked-up version:

With this arrangement, during the period when the control pulse CONT is at an "L" level, the transfer pulses ~~ϕ LH1c~~ ϕ H1c and ~~ϕ LH2c~~ ϕ H2c are not supplied to the transfer registers 22R, 22G, and 22B, thus it is possible to stop the transfer operations of the transfer registers 22R, 22G, and 22B of the color sensor 22 during the period of the second read-out of the monochrome sensor 10.

Clean version:

With this arrangement, during the period when the control pulse CONT is at an "L" level, the transfer pulses ϕ H1c and ϕ H2c are not supplied to the transfer registers 22R, 22G, and 22B, thus it is possible to stop the transfer operations of the transfer registers 22R, 22G, and 22B of the color sensor 22 during the period of the second read-out of the monochrome sensor 10.

15. AES 11/8/07

On page 14, 1st full paragraph, of the Specification, please amend:

Marked-up version:

In this regard, even though the transfer operations of the transfer registers 22R, 22G, and 22B of the color side are stopped, the two-phase transfer pulses ~~ϕ H1c~~ ϕ LH1c and ~~ϕ H2c~~ ϕ LH2c continue to be applied to a predetermined number of the transfer stages, that is, the transfer stages for several bits, including the final transfer stage among the transfer registers 22R, 22G, and 22B.

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Clean version:

In this regard, even though the transfer operations of the transfer registers 22R, 22G, and 22B of the color side are stopped, the two-phase transfer pulses ϕ LH1c and ϕ LH2c continue to be applied to a predetermined number of the transfer stages, that is, the transfer stages for several bits, including the final transfer stage among the transfer registers 22R, 22G, and 22B.

15, AEG 11/8/07
On page 14, 2nd full paragraph, of the Specification, please amend:

Marked-up version:

After this, there is no signal charge in the transfer stages for several bits: however, the transfer pulses ϕ H1e ϕ LH1c and ϕ H2e ϕ LH2c are continuously applied.

Clean version:

After this, there is no signal charge in the transfer stages for several bits: however, the transfer pulses ϕ LH1c and ϕ LH2c are continuously applied.